

Fig. 3

FIG. 4A

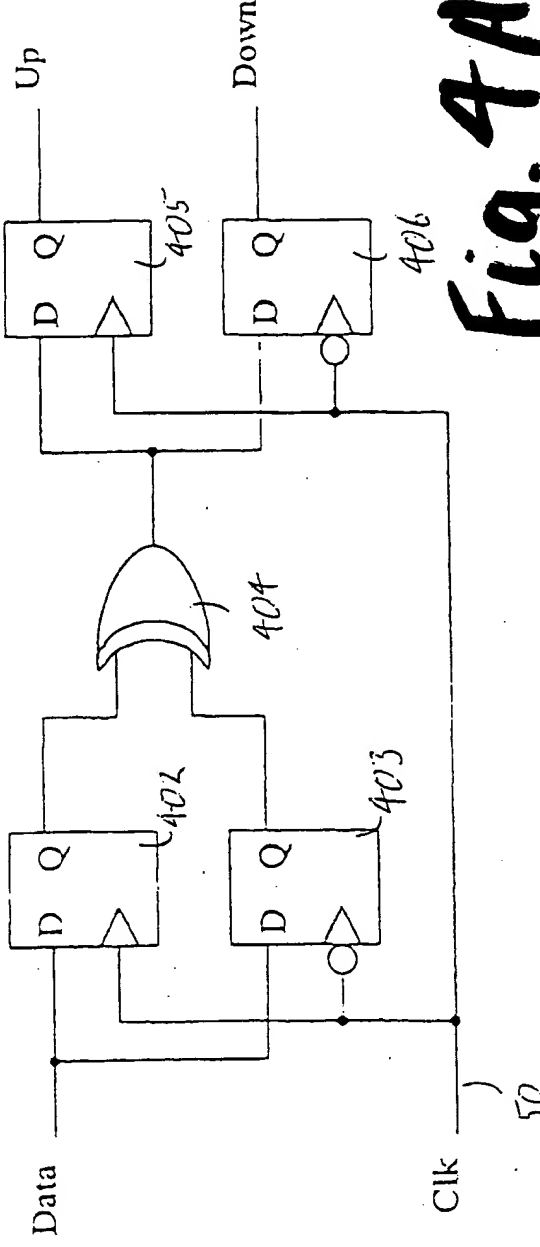


Fig. 4A

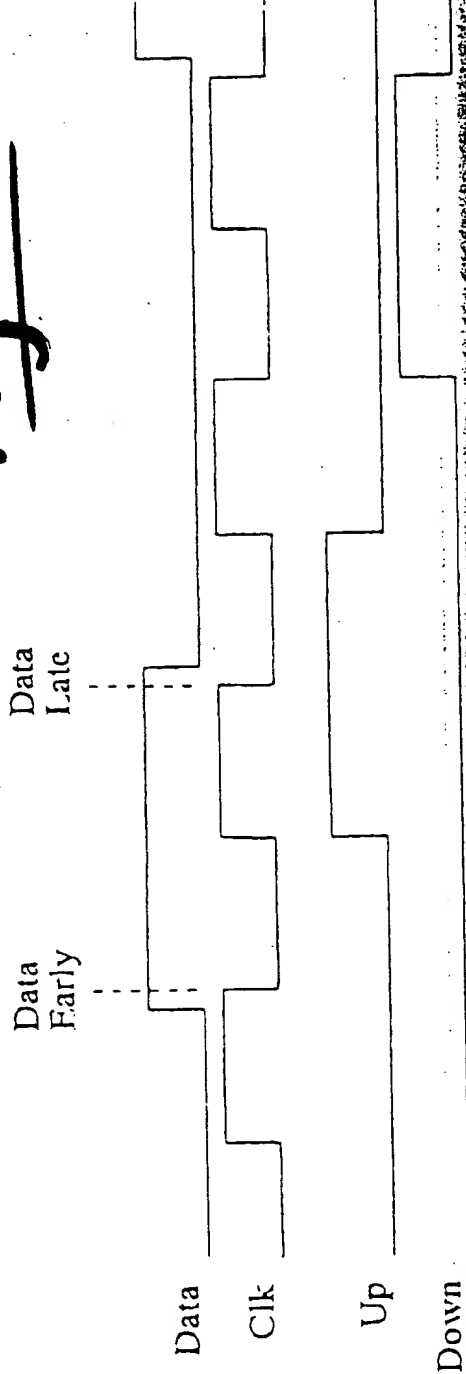


Fig. 4B

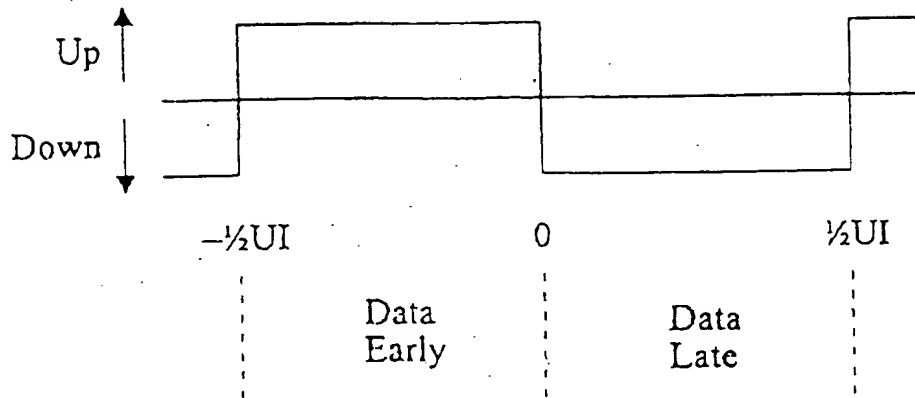


Fig. 5

Data delay adjustment range

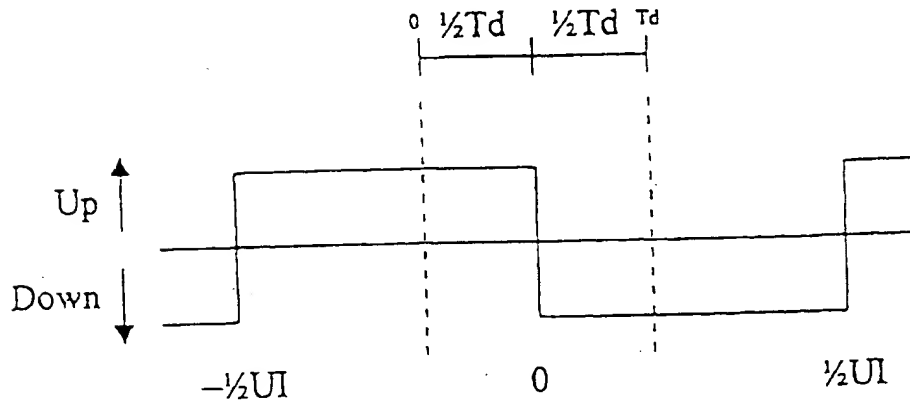


Fig. 6

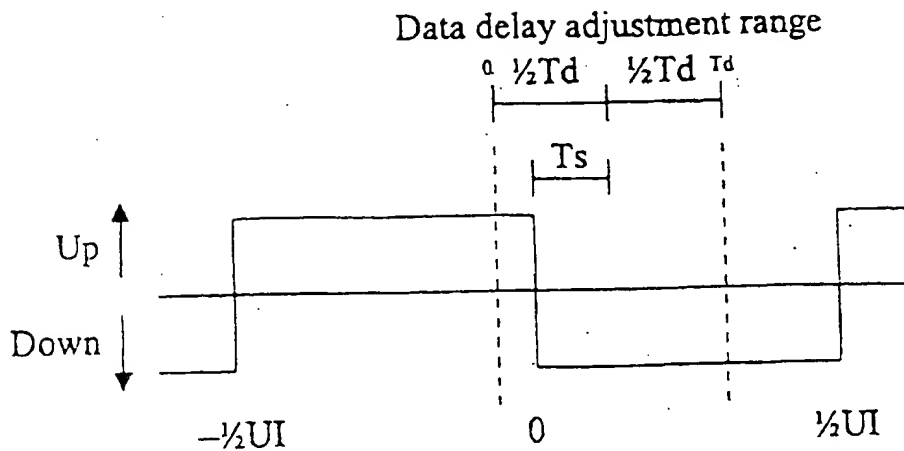


Fig. 7

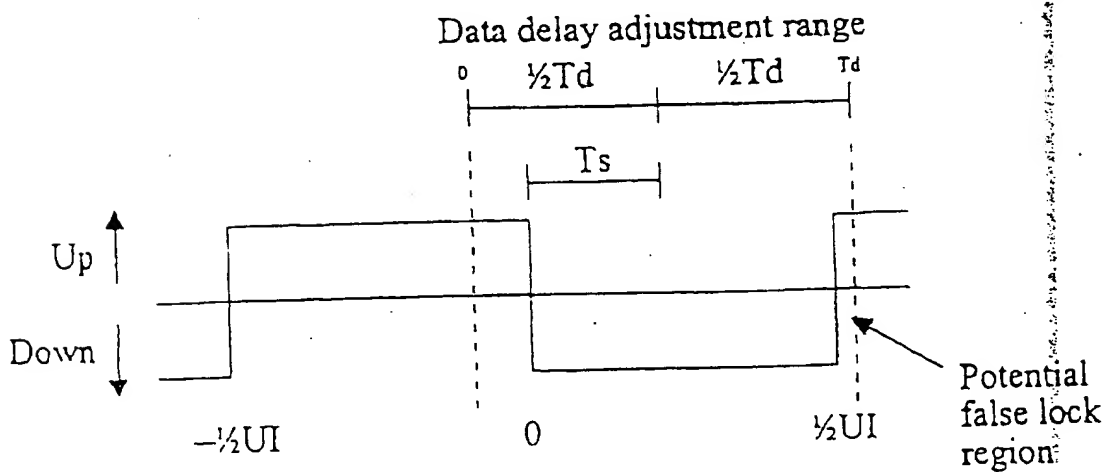


Fig. 8

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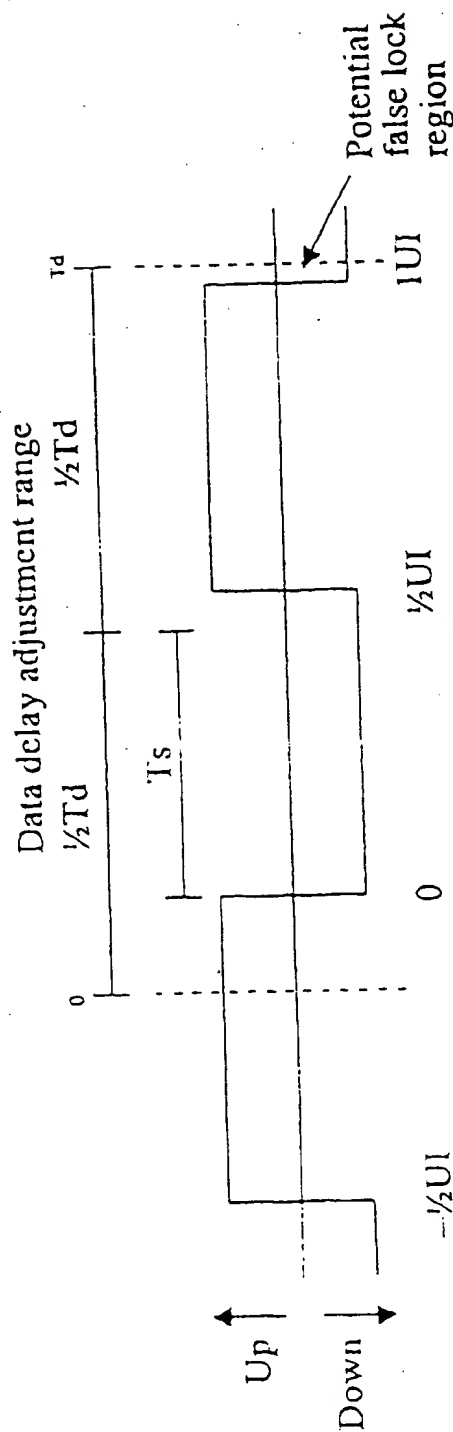


Fig. 9

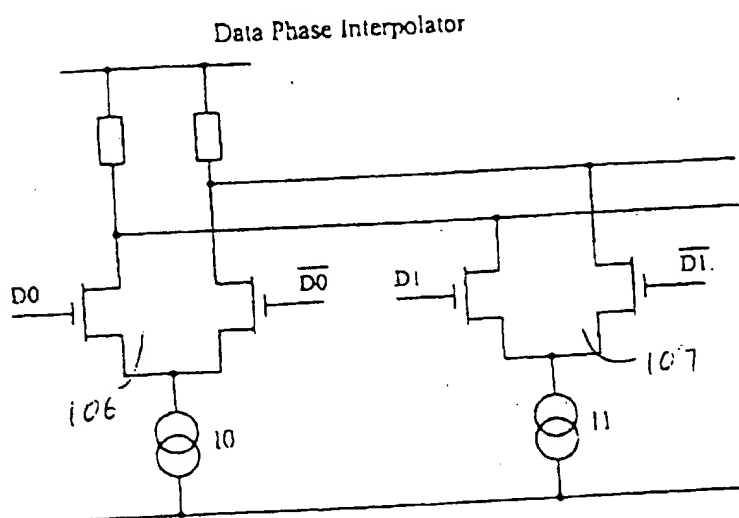
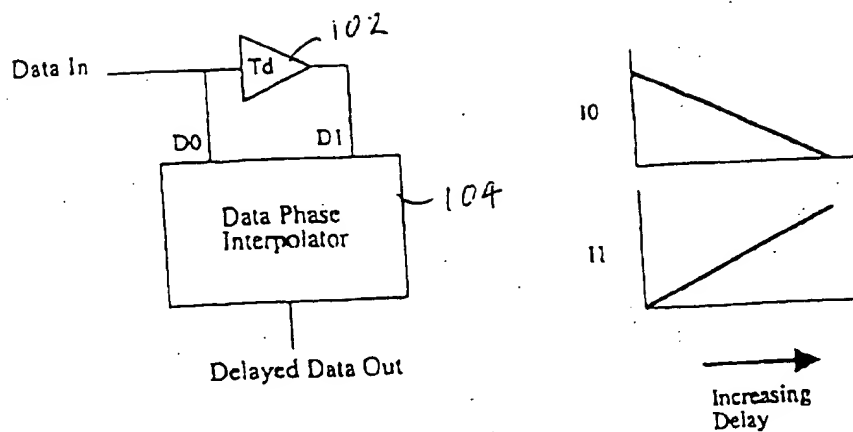


Fig. 10

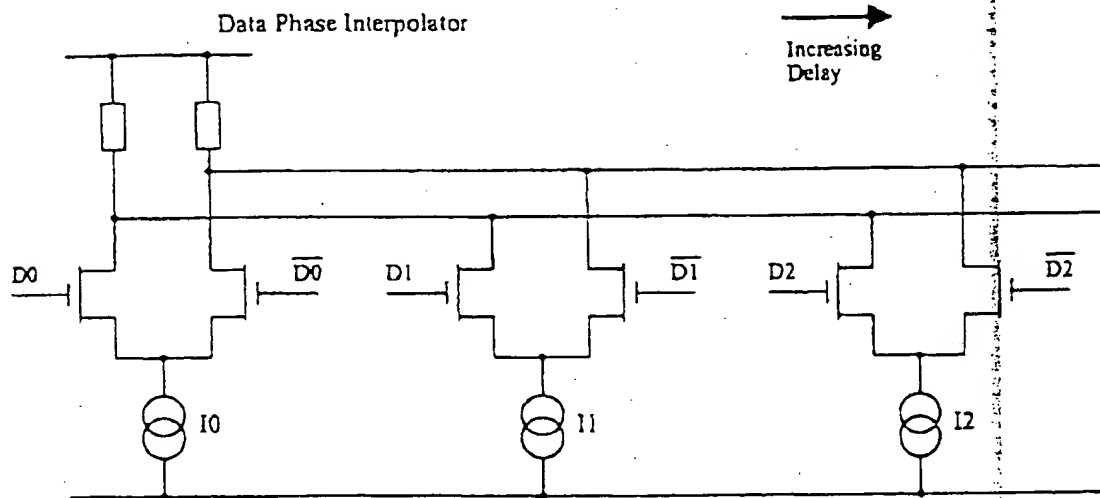
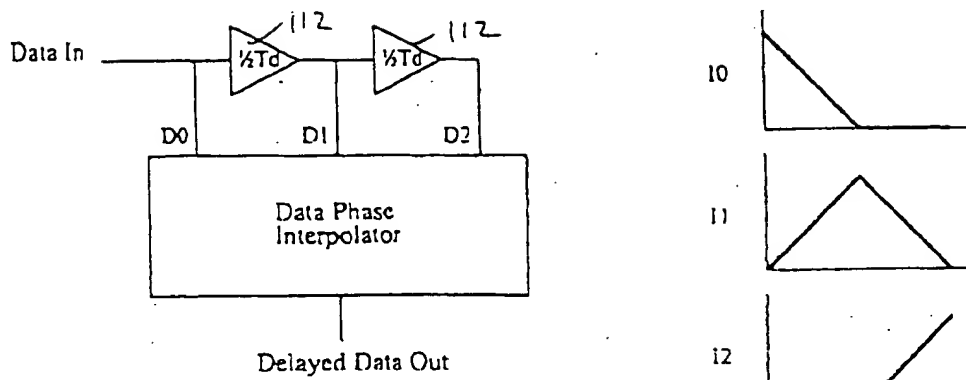


Fig. 11